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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

Complete if Known					
Application / Conf. No.	10/618,404 / 5533				
Filing Date	July 11, 2003				
First Named Inventor	Steven P. Young				
Art Unit	2819				
Examiner Name	James Hyonchoi Cho				
Attorney Docket Number	X-1392 US				

	U.S. PATENT DOCUMENTS							
Examiner Initials *	Cite No.1	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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	***************************************	US- 5,394,443	02-28-95	Byers et al.				
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- Calabrahalar (Carolina Calabrahan Carolina Car	***************************************	US- 6,204,689 B1	03-20-01	Percy et al.				
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	***********************	US- 6,538,470	03-25-03	Langhammer et al.	<u> </u>			
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	FOREIGN PATENT DOCUMENTS										
Examiner Initials *	Cite No.¹	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear						
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document							
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	STATEMENT BY APPLICAN			Art Unit	2819				
	(use as many sheets as necessary)	ecessary)	Examiner Name	James Hyonch	oi Cho				
	Sheet		of	2	Attorney Docket Number	X-1392 US			

OTHER NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No ⁻¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Τ²			
		Xilinx, Inc.; DS031-2 (v1.9) Advance Product Specification; "Virtex-II Platform FPGA Handbook," available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; November 29, 2001, pp. 77-79.				
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